

JAN 27 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Deshpande et al.

Serial No.: 10/709,048

Filed: April 8, 2004

Group Art Unit: 2814

Examiner: Ingham, John C.

Atty. Docket No.: FIS920030397US1

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Duane N. Moore

For: A MANUFACTURABLE METHOD AND STRUCTURE FOR DOUBLE SPACER  
CMOS WITH OPTIMIZED NFET/PFET PERFORMANCE

Commissioner of Patents  
PO BOX 1450  
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action mailed October 28, 2005, please amend the above-  
identified patent application as follows:

**IN THE CLAIMS:**

Please cancel claims 15-20 and amend the remaining claims as follows: